

CONTROL OF THE ACCESS TO A MEMORY INTEGRATED WITH A
MICROPROCESSOR

Abstract

A method and a circuit for controlling the access to all or part of the content of a first memory integrated with a microprocessor, consisting of using a priority-holding interrupt, of using at least one register of keys, and of applying at least one access control algorithm contained in a second auxiliary memory and using the content of at least one also integrated storage element and the content of the key register, the content of the auxiliary memory being programmable only once.